AMENDMENTS TO THE SPECIFICATION

On page 28, line 21 and continuing to page 29, line 13, please replace the paragraph with the following amendment paragraph:

On the other hand, the liquid crystal panel 12 includes a plurality of scanning signal lines G1, G2, . . . and data signal lines D1, D2, . . . which cross each other, and further includes in each area surrounded by adjacent two scanning signal lines and the data signal lines, an electro-optical element and a switching element TFT11, TFT12, . . . (hereinafter simply referred to as TFT when it is not necessarily be ~~speicfied~~ specified) and pixel capacitor C11, C12, . . . , which correspond to the electro-optical element. The liquid crystal panel 12 is of the active matrix type wherein liquid crystal elements as electro-optical elements are driven/displayed by charges as input in the pixel capacitors C11, C12, . . . , by the switching elements TFT11, TFT12, . . . In FIGS. 6 to 10, the liquid crystal capacitors and the auxiliary capacitors together are referred to as the pixel capacitors C11, C12, . . .

On page 35, line 7, please replace the paragraph with the following amendment paragraph:

The present invention is also applicable to the dot inverse driving method, and in this case, a switch is provided as short-circuit means between adjacent pixels of mutually reverse polarities, and this switch can be functioned by conducting/driving

signal lines provided in common between adjacent pixels in parallel to the scanning signal line directly before scanning the scanning signal line G. According to the foregoing structure, the switch and the signal line are required on the side of the liquid crystal panel, the short circuiting is performed in the state where the scanning signal line G is not subjected to scanning, i.e., in the state where the TFT is cut-off, and each of the pixel capacitors C11, C12, . . . is separated from the data signal line D. It is therefore possible to adopt the ~~convention al~~ conventional data driver for the DATA driver DD without the separation switch 29.

On page 44, line 18 and continuing to page 45, line 9, please replace the paragraph with the following amendment paragraph:

Therefore, in the adjacent pixels of the pair, by carrying out selection/scanning of the scanning signal lines simultaneously in the blanking period directly before the selection-scanning period of the scanning signal line to be scanned first, only this scanning signal line is subjected to selection scanning after neutralizing ~~th~~ the charges of the pixel capacitors via the data signal ~~ine~~ line, and the data signal line is input into the pixel capacitor from the data signal line driving circuit. Then, in the blanking period directly before the selection-scanning period of the scanning signal line to be scanned next, both of the scanning signal lines in the pair are set in the non-selection state, and then only the scanning signal line to be scanned next is subjected to selection scanning, and the data signal is input into the pixel capacitor from the data signal line driving circuit.